

PROGRAMME

Monday, November 21th 2016

08:30	10:00	Registration and Coffee		
10:00	10:15	Introduction Minos	the Minos LabEx update	Olivier Joubert CNRS/LTM, F
10:15	11:30	Tutorial invited	Manufacturing mutitechno and multiproducts	Stéphane Hubac STMicroelectronics, F
11:30	12:45	Tutorial invited	Economy, driving force in Si microelectronics	Jean-Christophe Eloy Yole Development, F
12:45	14:15	Lunch		
14:15	15:30	Tutorial Invited	FDSOI Derivative, FinFET and other stuff	Jan Höntschel Global Foundries, D
15:30	16:45	Tutorial Invited	CoolCube and options	Maud Vinet CEA/Leti, F
16:45	17:15	Coffee Break		
17:15	18:30	Tutorial invited	Stacked Horizontal Nanowires	Sylvain Barraud CEA/Leti, F
18:30	19:15	Free Time	Informal discussions	
19:15	20:45	Dinner (invitation only)		

Tuesday, November 22th 2016

08:30	09:45	Tutorial invited	Upgrade in Memories concepts and realisation	Laurent Cario IMN, F
09:45	11:00	Tutorial invited	Vertical Nanowires for new applications	Guilhem Larrieu LAAS, F
11:00	11:30	Coffee Break		
11:30	11:50	Contribution	Selected Minos results	Philip Kraus LabEx Minos
11:50	13:05	Invited	Silicon process technology at the limits of materials	Applied Materials, US
13:05	14:35	Lunch		
14:35	15:50	Tutorial invited	Nano-technologies for neurosciences	Thibault Honegger CNRS/LTM, F
15:50	16:10	Contribution	Techno used for Qbit studies	Louis Hutin CEA/Leti, F
16:10	16:40	Tutorial invited	2D materials: an introduction *	Thanasis Dimoulas Demokritos, G and Lanef
16:40	17:10	Tutorial invited	New plasma dicing technology for advanced packaging of emerging devices *	David Lishan Plasma-Therm Company
17:10		End of the Workshop		

Wednesday, November 23th 2016

*: see specific planning of dedicated sessions, free attendance